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7590 09/28/2005

Blakely Sokoloff Taylor & Zafman LLP
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In Chung

(Depositor's name)

[Signature]

12/22/05

(Date)

01/03/2006 RFEKADU2 00000006 09651385

01 FC:1501 1400.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/651,385	08/29/2000	Sanjay Dabral	042390.P5258D	9681

TITLE OF INVENTION: DIODE AND TRANSISTOR DESIGN FOR HIGH SPEED I/O

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	12/28/2005
EXAMINER	ART UNIT		CLASS-SUBCLASS		
DIAZ, JOSE R	2815		257-355000		

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
 Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list
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1 Blakely, Sokoloff,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Intel Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Authorized Signature Edwin H. Taylor

Date 12/22/05

Typed or printed name Edwin H. Taylor

Registration No. 25,129

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